LSU CCT Professor Thomas Sterling to Deliver Keynote Speech at ISC in Germany

Thomas Sterling, Seola Arnaud and Richard Vernon Edwards Jr. Professor in Computer Science and the LSU Center for Computation & Technology, will deliver a keynote address at the International SuperComputing Conference, which takes place June 19-23 in Hamburg, Germany.

This conference serves as a worldwide forum for scientists, IT-decision makers, and technology leaders to discuss the newest technology for high performance computing, networking, and storage.

Sterling's keynote address, "HPC Achievement & Impact 2011," will give a retrospective review of the achievements and impact of the progress made during the preceding 12 months. It will examine the dramatic leaps in technologies, deployed systems, breakthrough computational accomplishments, and new methods and tools. It will also track the growth and trends in performance opportunity by taking an in-depth look at the new multicore chip architectures that are the building blocks of many of the prominent supercomputers worldwide.

Sterling, who is probably best known as the father of Beowulf clusters and his research on Petaflops computing architecture, will reflect on the past computational science accomplishments to look forward through the eyes of new initiatives building toward the extremes of performance, even to Exaflops.

"This year has seen dramatic changes in the field of HPC with increased emphasis on GPU acceleration and a new international player, China, assuming the number one spot of fastest computer in the world. This summer's ISC meeting in Hamburg, the second largest supercomputing conference in the world, will be extremely exciting and the place to be in June," said Sterling.

Recently titled a "Rock Star of HPC" by insideHPC and a "Mover & Shaker" by HPCwire, Sterling's current research focuses on associative template dataflow; Beowulf class PC/Linux cluster computing; continuum computer architecture; petaflops computing; hybrid technology multi-threaded architecture exploiting extremes in high capability and capacity device technologies; and advanced processor-in-memory architecture incorporating message-driven multi-threaded execution.

Sterling's address will complement presentations by three other prominent speakers, Henry Markram from EPFL, Switzerland; Philippe Vannier, Bull, France; and Dean Klein, Micron Technology, United States.

For more information on the International Supercomputing Conference, please visit http://www.supercomp.de/isc11/.

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